FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)

Date Submitted to PTO: October 15, 2002

ATTY DOCKET NO.	SERIAL NO.
2001_1828	10/017,392

OCT 1 5 2002

APPLICANT
Yuusaku OHTA et al.

FILING DATE	GROUP
December 18, 2001	2131

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

RECEIVED
OCT 1 7 2002
Technology Center 2100

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
TT	AJ	00/30262	5/2000	WO				
	AK							
	AL							
	AM							
	AN							

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

TT	AO	WASSAL A.G., et al., "A VLSI ARCHITECTURE FOR ATM ALGORITHM-AGILE ENCRYPTION", Proceedings Great Lakes Symposium on VLSI, March 4, 1999, pages 325-328.
	AP	
	AQ	

EXAMINER	DATE CONSIDERED 05.05.05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO 1449 (modified)

**U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE**

LIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)

Date Submitted to PTO: December 18, 2001

| ATTY DOCKET NO. | SERIAL NO. |
| 2001_1828A | NEW |

| APPLICANT |
| Yuusaku OHTA et al. |

| FILING DATE | GROUP |
| December 18, 2001 | |

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	AJ							
	AK							
	AL							
	AM							
	AN							

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

TT	AO	"ADSP-2141L Safenet DSP Security System on a Chip" by Analog Devices, pp. 1-5.
	AP	
	AQ	

| EXAMINER | DATE CONSIDERED 05.05.05 |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.